

IN THE CLAIMS:

Please amend Claims 1, 4, 6, 8, and 9 as shown in this complete set of all pending Claims:

1. (Currently amended) A communication receiver apparatus, comprising:
 - an input for receiving from a communication transmitter apparatus an input analog communication signal;
 - a feedforward equalizer coupled to said input for producing in response to said input analog communication signal an equalized analog communication signal;
 - a sampler coupled to said feedforward equalizer for producing digital communication information in response to said equalized analog communication signal; [[and]]
 - a feedback equalizer coupled between said sampler and said feedforward equalizer for controlling said feedforward equalizer in response to said digital communication information;
wherein said feedback equalizer includes a digital-to-analog conversion portion having an input coupled to said sampler for receiving said digital communication information, said digital-to-analog conversion portion having an output coupled to said feedforward equalizer;
wherein said digital-to-analog conversion portion includes a plurality of digital-to-analog converters having respective inputs coupled to said sampler and having respective outputs coupled to said feedforward equalizer.
2. (Original) The apparatus of Claim 1, wherein said feedforward equalizer includes a wire summation node.
3. (Canceled)
4. (Currently amended) The apparatus of Claim [[3]] 1, wherein said feedforward equalizer includes a wire summation node.
5. (Canceled)

6. (Currently amended) The apparatus of Claim [[5]] 1, wherein each of said digital-to-analog converters includes a current source digital-to-analog converter.
7. (Original) The apparatus of Claim 6, wherein said outputs of said digital-to-analog converters are connected together at an input of said feedforward equalizer.
8. (Currently amended) The apparatus of Claim [[5]] 1, wherein said feedforward equalizer includes a wire summation node.
9. (Currently amended) The apparatus of Claim [[5]] 1, wherein said feedback equalizer includes a delay apparatus coupled between said sampler and said digital-to-analog converters for providing said digital communication information to said digital-to-analog converters at respectively different points in time.
10. (Original) The apparatus of Claim 1, wherein said input analog communication signal carries a SONET communication.
11. (Original) The apparatus of Claim 1, wherein said feedback equalizer includes a control input for receiving first control information, said feedback equalizer responsive to said control information for controlling said feedforward equalizer, said control information designed to minimize interference at temporal boundaries between data symbols carried by said equalized analog communication signal.
12. (Original) The apparatus of Claim 11, wherein said input analog communication signal is produced by the communication transmitter apparatus in response to second control information, said first control information designed in conjunction with the second control information to minimize interference at points in time between said temporal boundaries.
- 13-25. (Canceled)